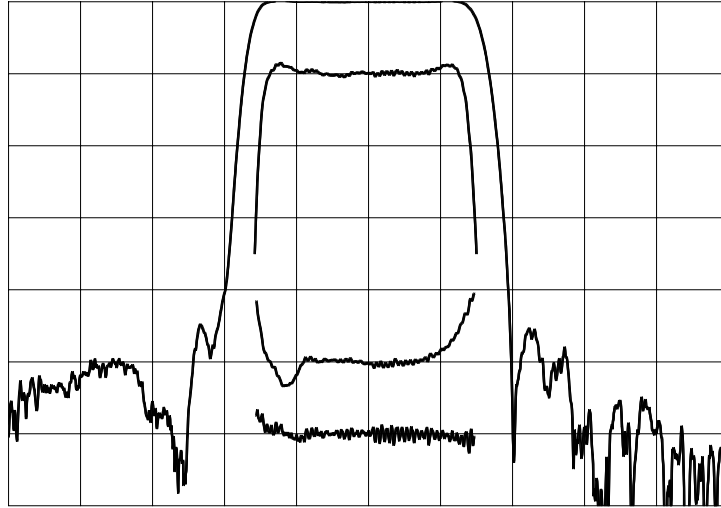


**TYPICAL PERFORMANCE**

**PRELIMINARY**



Horizontal: 8 MHz/div

Vertical (from top):  
 Magnitude 10 dB/div  
 Magnitude 1 dB/div  
 Phase deviation 5 deg/div  
 Group Delay 50 ns/div

**SPECIFICATION**

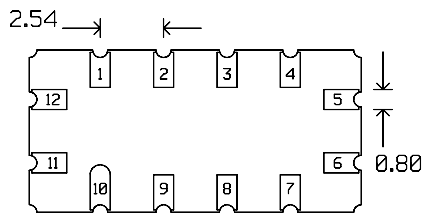
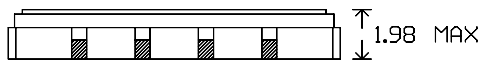
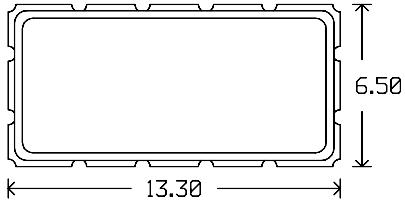
Parameter	Min	Typ	Max	Units
Center Frequency (Fc) <sup>1</sup>		96		MHz
Insertion Loss <sup>2</sup>		14	15.5	dB
Amplitude Ripple <sup>3</sup>		0.3	1	dB
Amplitude Ripple in any 3.84 MHz band <sup>3</sup>		0.3	0.6	dB
0.6 dB Bandwidth <sup>4</sup>	20	22.8		MHz
Phase Linearity <sup>3</sup>		3	7	deg
Group Delay Variation <sup>3</sup>		15	90	ns
Group Delay Variation in any 3.84 MHz band <sup>3</sup>		15	50	ns
Absolute Delay		0.89		us
40 dB Bandwidth <sup>4</sup>		31.5	35	MHz
Rejection, 0.3~10 MHz <sup>4</sup>	25	50		dB
Rejection, 10~68 MHz <sup>4</sup>	45	50		dB
Rejection, 68~78.5 MHz <sup>4</sup>	40	45		dB
Rejection, 113.5~124 MHz <sup>4</sup>	40	45		dB
Rejection, 124~160 MHz <sup>4</sup>	45	52		dB
Rejection, 160~500 MHz <sup>4</sup>	25	27		dB
Temperature Coefficient of Frequency		-90		ppm/°C
Temperature Range	-40		+85	°C
System Source and Load Impedance		50		Ω

- Notes: 1. Average of lower & upper 3 dB frequencies (3 dB measured from peak).  
 2. Average over 96±10 MHz.  
 3. Evaluated over 96±10 MHz.  
 4. Reference 0 dB at insertion loss level.

Micro Networks, 324 Clark Street, Worcester, MA 01606, USA tel: 508-852-5400, fax: 508-852-8456, www.micronetworks.com

**PACKAGE OUTLINE**

**PRELIMINARY**

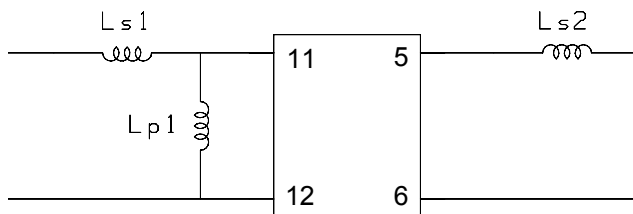


**Units:** mm

**Pin Configuration:**

Input: 11  
Input Return: 12  
Output: 5  
Output Return: 6  
Ground: 1,2,3,4,7,8,9,10

**MATCHING CIRCUIT**



Component values in 50 Ω: Lp1 = 820 nH                      Ls2 = 68 nH  
(Minimum Q = 45)                      Ls1 = 82 nH

**Notes**

- Optimum component values may change depending on board layout. The values shown here are intended as a guide only.